

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	"714565".pn.	US-PGPUB; USPAT	OR	OFF	2008/01/29 13:04
L2	5	"714565".ap.	US-PGPUB; USPAT	OR	OFF	2008/01/29 13:06
L3	1	"7117284".pn.	US-PGPUB; USPAT	OR	OFF	2008/01/29 13:22
L4	8	"714563".ap.	US-PGPUB; USPAT	OR	OFF	2008/01/29 13:06
L5	388	(726/30).CCLS.	US-PGPUB; USPAT	OR	OFF	2008/01/29 13:23
L6	342	(726/21).CCLS.	US-PGPUB; USPAT	OR	OFF	2008/01/29 13:23
L7	516	(713/166).CCLS.	US-PGPUB; USPAT	OR	OFF	2008/01/29 13:23
L8	2114	(713/193).CCLS.	US-PGPUB; USPAT	OR	OFF	2008/01/29 13:23
L9	228	(710/261).CCLS.	US-PGPUB; USPAT	OR	OFF	2008/01/29 13:23
L10	2297	(711/162).CCLS.	US-PGPUB; USPAT	OR	OFF	2008/01/29 13:23
L11	616	(712/244).CCLS.	US-PGPUB; USPAT	OR	OFF	2008/01/29 13:23
L12	249	5 and @ay <="2002"	US-PGPUB; USPAT	OR	OFF	2008/01/29 13:24
L13	257	6 and @ay <="2002"	US-PGPUB; USPAT	OR	OFF	2008/01/29 13:24
L14	304	7 and @ay <="2002"	US-PGPUB; USPAT	OR	OFF	2008/01/29 13:24
L15	1229	8 and @ay <="2002"	US-PGPUB; USPAT	OR	OFF	2008/01/29 13:24
L16	201	9 and @ay <="2002"	US-PGPUB; USPAT	OR	OFF	2008/01/29 13:24
L17	1027	10 and @ay <="2002"	US-PGPUB; USPAT	OR	OFF	2008/01/29 13:24
L18	507	11 and @ay <="2002"	US-PGPUB; USPAT	OR	OFF	2008/01/29 13:25
L19	30	watt-simon-charles.in.	US-PGPUB; USPAT	OR	OFF	2008/01/29 13:25

EAST Search History

L20	30	L19	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/29 13:26
L21	42031	programm\$4 with register\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/01/29 13:26
L22	42031	L21	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/29 13:26
L23	4	22 and 12	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/29 13:26
L24	7	22 and 13	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/29 13:27
L25	12	22 and 14	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/29 13:27
L26	51	22 and 15	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/29 13:27

EAST Search History

L27	49	22 and 16	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/29 13:27
L28	12	22 and 17	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/29 13:27
L29	82	22 and 18	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/01/29 13:27
L30	1	23 and (parameter)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/29 13:29
L31	4	24 and (parameter)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/29 13:28
L32	8	25 and (parameter)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/29 13:28
L33	13	26 and (parameter)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/29 13:28

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L34	20	27 and (parameter)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/29 13:28
L35	1	28 and (parameter)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/29 13:28
L36	22	29 and (parameter)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/29 13:28
L37	0	23 and (programm\$4 near5(parameter))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/29 13:29
L38	1	24 and (programm\$4 near5(parameter))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/29 13:30
L39	0	25 and (programm\$4 near5(parameter))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/29 13:29
L40	3	26 and (programm\$4 near5(parameter))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/29 13:30

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L41	6	27 and (programm\$4 near5(parameter))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/29 13:30
L42	1	28 and (programm\$4 near5(parameter))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/29 13:30
L43	1	29 and (programm\$4 near5(parameter))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/29 13:30
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EAST Search History

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S3	2	"7149862".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/12 13:57

EAST Search History

S4	2	"20020188831".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/12 14:00
S5	2	"5574786".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/12 14:01
S6	2	"7020767".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/12 14:02
S7	2	"6581162".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/12 14:03
S8	2	"4672572".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/12 14:04
S9	2	"20030101322".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/12 14:16
S10	2	"7117284".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/12 14:18
S11	2	"714519".ap.	US-PGPUB; USPAT	OR	OFF	2007/01/12 14:24

EAST Search History

S12	7	"714563".ap.	US-PGPUB; USPAT	OR	OFF	2007/01/12 14:31
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S15	16	("20020002673" "20030140205" "20030200402" "20030200405" "5339437" "5513349" "5530932" "5596755" "5615263" "5684948" "5724027" "5987604" "6327652" "6330670" "6775779" "6854046").PN. OR ("7165135").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/01/16 12:31
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S17	175	(710/269).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/09 16:53
S18	1644	(713/193).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/22 11:43
S19	1644	(713/193).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/01/22 11:43
S20	4	"714565".ap.	US-PGPUB; USPAT	OR	OFF	2007/07/03 11:37
S21	4	"714565".ap.	US-PGPUB; USPAT	OR	OFF	2007/07/06 18:30
S22	27	watt-simon-charles.in.	US-PGPUB; USPAT	OR	OFF	2007/07/06 15:17

EAST Search History

S23	1	"7165135".pn.	USPAT	OR	OFF	2007/07/09 16:51
S24	1281	(726/26).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/09 16:52
S25	695	S24 and @ay <='2002'	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 16:52
S26	184	(710/269).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/09 16:53
S28	144	S26 and @ay <='2002'	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/09 16:54
S29	1872	(713/193).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/09 16:54
S30	1151	S29 and @ay <='2002'	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/09 16:54
S31	39751	programm\$4 with register\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/09 16:55

EAST Search History

S32	27	S31 and S25	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/09 16:55
S33	45	S31 and S28	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/09 16:55
S34	49	S31 and S30	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/09 16:55
S35	1	"7117284".pn.	USPAT	OR	OFF	2008/01/23 13:25
S36	5	"714565".ap.	US-PGPUB; USPAT	OR	OFF	2008/01/23 13:25
S37	5	"714565".ap.	US-PGPUB; USPAT	OR	OFF	2008/01/24 11:53

Interference search

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L44	0	((processor) and (plurality modes) and (plurality domains) and (secure domain) and (non-secure domain) and (exception condition) and (plural programmable parameters) and (exception trap mask register) and (secure mode exception handler) and (exception handler)).clm.	US-PGPUB; USPAT	ADJ	ON	2008/01/29 13:33



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1 [Automotive Software Development for a Multi-Core System-on-a-Chip](#)

Hermann Kopetz, Roman Obermaisser, Christian El Salloum, Bernhard Huber

May 2007 **SEAS '07**: Proceedings of the 4th International Workshop on Software Engineering for Automotive Systems

Publisher: IEEE Computer Society

Full text available: pdf(229.67 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

There are many economic and technical arguments for the reduction of the number of Electronic Control Units (ECUs) aboard a car. One of the key obstacles to achieve this goal is the limited composability, fault isolation and error containment of today's ...

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2 [Flight data recorder: monitoring persistent-state interactions to improve systems management](#)

Chad Verbowski, Emre Kiciman, Arunvijay Kumar, Brad Daniels, Shan Lu, Juhan Lee, Yi-Min Wang, Roussi Roussev

November 2006 **OSDI '06**: Proceedings of the 7th symposium on Operating systems design and implementation

Publisher: USENIX Association

Full text available: pdf(457.56 KB) Additional Information: [full citation](#), [abstract](#), [references](#)

Mismanagement of the persistent state of a system---all the executable files, configuration settings and other data that govern how a system functions---causes reliability problems, security vulnerabilities, and drives up operation costs. Recent research ...

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3 [System-level power optimization: techniques and tools](#)


Luca Benini, Giovanni de Micheli

April 2000 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 5 Issue 2

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Publisher: ACM

Full text available:  [pdf\(385.22 KB\)](#) Additional Information: [full citation](#), [abstract](#),
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This tutorial surveys design methods for energy-efficient system-level design. We consider electronic systems consisting of a hardware platform and software layers. We consider the three major constituents of hardware that consume energy, namely computation, ...

4 Proof verification and the hardness of approximation problems

 Sanjeev Arora, Carsten Lund, Rajeev Motwani, Madhu Sudan, Mario Szegedy

May 1998 **Journal of the ACM (JACM)**, Volume 45 Issue 3


Publisher: ACM

Full text available:  [pdf\(418.87 KB\)](#) Additional Information: [full citation](#), [abstract](#),
[references](#), [cited by](#), [index terms](#)

We show that every language in NP has a probabilistic verifier that checks membership proofs for it using logarithmic number of random bits and by examining a constant number of bits in the proof. If a string is in the language, then ...


Keywords: NP-completeness, optimization, proof verification, randomness

5 Exterminator: automatically correcting memory errors with high probability

 Gene Novark, Emery D. Berger, Benjamin G. Zorn

June 2007 **PLDI '07: ACM SIGPLAN Notices**, Volume 42 Issue 6


Publisher: ACM

Full text available:  [pdf\(521.40 KB\)](#) Additional Information: [full citation](#), [abstract](#),
[references](#), [index terms](#)

Programs written in C and C++ are susceptible to memory errors, including buffer overflows and dangling pointers. These errors, which can lead to crashes, erroneous execution, and security vulnerabilities, are notoriously costly to repair. Tracking down ...


Keywords: dieFast, dynamic memory allocation, error correction, exterminator, memory errors, probabilistic algorithms, randomized algorithms

6 Efficient scheduling of conditional behaviors for high-level synthesis

 Apostolos A. Kountouris, Christophe Wolinski

July 2002 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 7 Issue 3

Publisher: ACM


Full text available:  [pdf\(1.50 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#),
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As hardware designs get increasingly complex and time-to-market constraints get tighter there is strong motivation for high-level synthesis

(HLS). HLS must efficiently handle both dataflow-dominated and controlflow-dominated designs as well as designs ...

Keywords: Design automation, conditional behavior, high level synthesis (HLS), scheduling

7 Exterminator: automatically correcting memory errors with high probability


 Gene Novark, Emery D. Berger, Benjamin G. Zorn
June 2007 **PLDI '07**: Proceedings of the 2007 ACM SIGPLAN conference on Programming language design and implementation
Publisher: ACM


Full text available:  [pdf\(521.40 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Programs written in C and C++ are susceptible to memory errors, including buffer overflows and dangling pointers. These errors, which can lead to crashes, erroneous execution, and security vulnerabilities, are notoriously costly to repair. Tracking down ...

Keywords: dieFast, dynamic memory allocation, error correction, exterminator, memory errors, probabilistic algorithms, randomized algorithms

8 AniAniWeb: a wiki approach to personal home pages


 Jochen Rick
October 2007 **WikiSym '07**: Proceedings of the 2007 international symposium on Wikis
Publisher: ACM


Full text available:  [pdf\(2.93 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This article reports on my dissertation research on personal home pages. It focuses on the design of AniAniWeb, a server-based system for authoring personal home pages. AniAniWeb builds on a wiki foundation to address many of the limitations of static ...

Keywords: AniAniWeb, access control, personal home pages, wiki design

9 Spatial routines for a simulated speech-controlled vehicle

 Stefanie Tellex, Deb Roy
March 2006 **HRI '06**: Proceeding of the 1st ACM SIGCHI/SIGART conference on Human-robot interaction
Publisher: ACM


Full text available:  [pdf\(630.38 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

We have defined a lexicon of words in terms of *spatial routines*, and used that lexicon to build a speech controlled vehicle in a simulator. A spatial routine is a script composed from a set of primitive operations on occupancy grids, analogous ...


Keywords: language grounding, situated language processing, spatial

language, spatial routines, visual routines, wheelchair

10 The development of the Emerald programming language

 Andrew P. Black, Norman C. Hutchinson, Eric Jul, Henry M. Levy
June 2007 **HOPL III**: Proceedings of the third ACM SIGPLAN conference on
History of programming languages

Publisher: ACM

Full text available:  [pdf\(1.45 MB\)](#) Additional Information: [full citation](#), [appendices and supplements](#), [abstract](#), [references](#), [index terms](#)

Emerald is an object-based programming language and system designed and implemented in the Department of Computer Science at the University of Washington in the early and mid-1980s. The goal of Emerald was to simplify the construction of distributed ...

Keywords: Eden, Emerald, Washington, abstract types, call-by-move, distributed programming, mobility, object mobility, object-oriented programming, polymorphism, remote object invocation, remote procedure call, type conformity

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1 [Building Intrusion-Tolerant Secure Software](#)

Tao Zhang, Xiaotong Zhuang, Santosh Pande

March 2005 **CGO '05**: Proceedings of the international symposium on Code generation and optimization

Publisher: IEEE Computer Society

Full text available: [pdf\(234.98 KB\)](#) Additional Information: [full citation](#), [abstract](#), [index terms](#)

In this work, we develop a secret sharing based compiler solution to achieve confidentiality, integrity and availability (intrusion tolerance) of critical data together, rather than tackling them one by one as in previous approaches. Under our scheme, ...

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[Dijkstra's Algorithm](#)

White Paper:
Explore profitable new approach to TDM-to-Packet migration
[www.ciena.com](#)

2 [Enforcing access control over data streams](#)

Barbara Carminati, Elena Ferrari, Kian Lee Tan

June 2007 **SACMAT '07**: Proceedings of the 12th ACM symposium on Access control models and technologies

Publisher: ACM

Full text available: [pdf\(248.38 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Access control is an important component of any computational system. However, it is only recently that mechanisms to guard against unauthorized access for streaming data have been proposed. In this paper, we study how to enforce the role-based access ...

Keywords: access control, data stream, security

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[Excel Genetic Algorithms](#)

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3 [Security as a new dimension in embedded system design](#)

Paul Kocher, Ruby Lee, Gary McGraw, Anand Raghunathan / Srivaths Ravi

June 2004 **DAC '04**: Proceedings of the 41st annual conference on Design automation

Publisher: ACM

Full text available: [pdf\(209.10 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

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The growing number of instances of breaches in information security in the last few years has created a compelling case for efforts towards secure electronic systems. Embedded systems, which will be ubiquitously used to capture, store, manipulate, and ...

Keywords: PDAs, architectures, battery life, cryptography, design, design methodologies, digital rights management, embedded systems, performance, security, security processing, security protocols, sensors, software attacks, tamper resistance, trusted computing, viruses


4 Securing sensitive content in a view-only file system



Kevin Borders, Xin Zhao, Atul Prakash

October 2006 **DRM '06: Proceedings of the ACM workshop on Digital rights management**

Publisher: ACM

Full text available:  [pdf\(357.44 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

One of the most fundamental problems in computer security is protecting sensitive digital information from unauthorized disclosure. There are a number of challenges, such as spyware, removable media, and mobile devices, which make this a very hard problem. ...

Keywords: digital rights management, file systems, information leakage, insider abuse, virtual machines

5 Compiler Optimizations to Reduce Security Overhead

Tao Zhang, Xiaotong Zhuang, Santosh Pande

March 2006 **CGO '06: Proceedings of the International Symposium on Code Generation and Optimization**

Publisher: IEEE Computer Society

Full text available:  [pdf\(384.79 KB\)](#) Additional Information: [full citation](#), [abstract](#), [index terms](#)


In this work, we present several compiler optimizations to reduce the overhead due to software protection. We first propose an aggressive rematerialization algorithm which attempts to maximally realize non-trusted values from other trusted values thereby ...

6 Security in mobile communications: challenges and opportunities

Audun Jøsang, Gunnar Sanderud

January 2003 **ACSW Frontiers '03: Proceedings of the Australasian information security workshop conference on ACSW frontiers 2003 - Volume 21**, Volume 21

Publisher: Australian Computer Society, Inc.

Full text available:  [pdf\(117.04 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

The nature of mobile communication, characterised for example by terminals having poor user interface and limited processing capacity, as well as complex combination of network protocols, makes the design of security solutions particularly challenging. ...



Keywords: heterogeneous networks, mobile devices, security, usability

7 Dynamic Tool Integration in Heterogeneous Computer Networks

Wolfgang Mueller, Tim Schattkowsky, Heinz-Josef Eikerling, Jan Wegner
March 2003 **DATE '03: Proceedings of the conference on Design,**

Automation and Test in Europe - Volume 1, Volume 1

Publisher: IEEE Computer Society

Full text available:  [pdf\(210.53 KB\)](#)  [Publisher Site](#)

Additional Information: [full citation](#),
[abstract](#),
[index terms](#)

Tool installation and automation of administrative tasks in heterogeneous computer networks becomes of increasing importance with the availability of complex heterogeneous computer networks. This article introduces a new approach for dynamic network ...

8 Using Branch Correlation to Identify Infeasible Paths for Anomaly

Detection

Xiaotong Zhuang, Tao Zhang, Santosh Pande


December 2006 **MICRO 39: Proceedings of the 39th Annual IEEE/ACM International Symposium on Microarchitecture**

Publisher: IEEE Computer Society

Full text available:  [pdf\(339.58 KB\)](#) Additional Information: [full citation](#), [abstract](#), [index terms](#)


In this paper, we propose a system called Infeasible Path Detection System (IPDS) to combat memory tampering attacks causing invalid program control flows. In our system, the compiler analyzes correlations between branches and then the analyzed information ...

9 XSLT transformation from UML models to LQN performance models

 Gordon P. Gu, Dorina C. Petriu

July 2002 **WOSP '02: Proceedings of the 3rd international workshop on Software and performance**

Publisher: ACM

Full text available:  [pdf\(197.54 KB\)](#) Additional Information: [full citation](#), [abstract](#),
[references](#), [cited by](#)

A graph grammar-based transformation of a UML design model into a Layered Queueing Network (LQN) performance model was previously proposed by the authors of this paper. The actual transformation was implemented in two ways: first by using an existing ...


Keywords: LQN, UML, XMI, XSLT, software performance engineering

10 Early evaluation of software performance based on the UML performance profile

Gordon Ping Gu, Dorina C. Petriu

October 2003 **CASCON '03: Proceedings of the 2003 conference of the Centre for Advanced Studies on Collaborative research**


Publisher: IBM Press

Full text available:  [pdf\(498.98 KB\)](#) Additional Information: [full citation](#), [abstract](#),
[references](#), [cited by](#), [index terms](#)

The "UML Profile for Schedulability, Performance and Time" recently adopted by OMG defines performance extensions via the Unified Modeling Language (UML) stereotypes, tagged values and constraints. In order to conduct quantitative performance analysis ...

11 Design and modelling of internode: a mobile provider provisioned VPN


Francisco Barceló, Josep Paradells, Fofy Setaki, Monique Gibeaux
February 2003 **Mobile Networks and Applications**, Volume 8 Issue 1
Publisher: Kluwer Academic Publishers


Full text available:  [pdf\(237.48 KB\)](#) Additional Information: [full citation](#), [abstract](#),
[references](#), [index terms](#)

This paper presents the design and architecture of a mobile Provider Provisioned VPN (PPVPN) together with a performance evaluation oriented model that allows first estimates of the VPN set-up delay to be computed. At the same time, some consequences ...

Keywords: IPSec, VPN, mobile IP, mobile VPN, provider provisioned VPN

12 Extending the transaction level modeling approach for fast communication architecture exploration

 Sudeep Pasricha, Nikil Dutt, Mohamed Ben-Romdhane
June 2004 **DAC '04: Proceedings of the 41st annual conference on Design automation**
Publisher: ACM


Full text available:  [pdf\(409.83 KB\)](#) Additional Information: [full citation](#), [abstract](#),
[references](#), [cited by](#), [index terms](#)

System-on-Chip (SoC) designs are increasingly becoming more complex. Efficient on-chip communication architectures are critical for achieving desired performance in these systems. System designers typically use Bus Cycle Accurate (BCA) models written ...

Keywords: AMBA, bus cycle accurate modeling, communication architecture exploration, shared bus architectures, transaction level modeling

13 Software product lines: a pedagogical application

John M. Hunt, John D. McGregor
December 2006 **Journal of Computing Sciences in Colleges**, Volume 22
Issue 2
Publisher: Consortium for Computing Sciences in Colleges

Full text available:  [pdf\(204.68 KB\)](#) Additional Information: [full citation](#), [abstract](#),
[references](#), [index terms](#)

This paper provides an overview of Software Product Lines and discusses


issues involved in using Software Product Lines in courses. An SPL designs and produces common assets for a group of related products as a family; rather than building the products ...

14 Proxy-based task partitioning of watermarking algorithms for reducing energy consumption in mobile devices



Arun Kejariwal, Sumit Gupta, Alexandru Nicolau, Nikil Dutt, Rajesh Gupta
June 2004 **DAC '04**: Proceedings of the 41st annual conference on Design automation

Publisher: ACM

Full text available:  [pdf\(494.20 KB\)](#) Additional Information: [full citation](#), [abstract](#),
[references](#); [index terms](#)

Digital watermarking is a process that embeds an imperceptible signature or watermark in a digital file containing audio, image, text or video data. The watermark is later used to authenticate the data file and for tamper detection. It is particularly ...

Keywords: handhelds, partitioning, proxy, watermarking

Results 1 - 14 of 14

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